IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s)

HIROSHI SAKAYORI

Filed

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HEREWITH

Title

APPARATUS FOR TESTING INTEGRATED CIRCUITS

Serial No.

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Examiner

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Art Unit

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Atty. Docket No.: 40010348-03US

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Mail Stop APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Prior to examination of the above-noted divisional patent application filed herewith, please enter the following amendment.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Amendments to the Drawings begin on page 5 of this paper.

Remarks begin on page 6 of this paper.

An Appendix including amended drawings, replacement Sheets 1-10, is attached following page of this paper.